

Notice of References Cited	Application/Control No. 10/681,206		Applicant(s)/Patent Under Reexamination FUJI ET AL.	
	Examiner Mary C. Jacob		Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,842,729	01-2005	Sample et al.	703/24
*	B	US-6,732,068	05-2004	Sample et al.	703/24
*	C	US-6,694,464	02-2004	Quayle et al.	714/725
*	D	US-6,564,367	05-2003	Fujii et al.	716/16
*	E	US-6,496,886	12-2002	Osaka et al.	710/100
*	F	US-6,377,912	04-2002	Sample et al.	703/28
*	G	US-6,279,146	08-2001	Evans et al.	716/18
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Takahashi et al, "110-GB/s Simultaneous Bidirectional Transceiver Logic Synchronized With a System Clock", IEEE Journal of Solid-State Circuits, Vol, 34, No. 11, November 1999
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.